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APPLICATION NO.	F	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
09/918,831	08/01/2001		Petrus Lambertus Adrianus Roelse	NL 000444	4772	
24737	7590	02/14/2005		EXAM	EXAMINER	
		CTUAL PROPE	PYZOCHA, MICHAEL J			
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BRIARCLIFF MANOR, NY 10510				ART UNIT	PAPER NUMBER	
				2137		
			DATE MAIL ED. 02/14/2005			

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)					
Office Action Summary	09/918,831	ROELSE, PETRUS LAMBERTUS ADRIANUS					
,	Examiner	Art Unit					
	Michael Pyzocha	2137					
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	86(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONED	nely filed s will be considered timely. the mailing date of this communication. O (35 U.S.C. § 133).					
Status							
1) Responsive to communication(s) filed on 01 Au	<u>ıgust 2001</u> .						
a) This action is FINAL . 2b) ⊠ This action is non-final.							
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is							
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claims							
4) Claim(s) <u>1-8</u> is/are pending in the application.							
4a) Of the above claim(s) is/are withdrawn from consideration.							
5) Claim(s) is/are allowed.							
6)⊠ Claim(s) <u>1-8</u> is/are rejected.							
7) Claim(s) is/are objected to.							
8) Claim(s) are subject to restriction and/or election requirement.							
Application Papers							
9) The specification is objected to by the Examine	r.	•					
10)⊠ The drawing(s) filed on <u>01 August 2001</u> is/are: a)⊠ accepted or b)⊡ objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11) The oath or declaration is objected to by the Ex	aminer. Note the attached Office	Action or form PTO-152.					
Priority under 35 U.S.C. § 119							
 12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 		-(d) or (f).					
2. Certified copies of the priority documents have been received in Application No							
3. Copies of the certified copies of the priority documents have been received in this National Stage							
application from the International Bureau	• • • • • • • • • • • • • • • • • • • •						
* See the attached detailed Office action for a list	of the certified copies not receive	d.					
Attachment(s) 1) Notice of References Cited (PTO-892)	4) \(\sum_\) Interview Summary	(DTO 442)					
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) Paper No(s)/Mail Date							
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date		atent Application (PTO-152)					

Application/Control Number: 09/918,831 Page 2

Art Unit: 2137

DETAILED ACTION

1. Claims 1-8 are pending.

Claim Objections

2. The phrase "(pseudo-) random" should be written either with "pseudo" or without to make it clear what the Applicant is claiming in claims 2, 3-5.

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claims 1, 3-4, 7-8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Rijmen et al (The Cipher SHARK) and further in view of Loureiro et al (Function Hiding Based on Error Correcting Codes).

As per claims 1 and 7, Rijmen et al discloses a method of generating a linear transformation matrix A for use in a symmetric-key cipher, the method including: generating a binary

Art Unit: 2137

(n,k,d) error-correcting code, represented by a generator matrix $\mathbf{G} \in \mathbf{Z}_2^{kxn}$ in a standard form $\mathbf{G} = (I_k \parallel B)$, with $B \in \mathbf{Z}_2^{kx(n-k)}$, where k < n < 2k, and d is the minimum distance of the binary error-correcting code (see page 4), and forming a nonsingular matrix (see page 5).

Rijmen et al fails to disclose extending matrix B with 2k-n columns, and deriving a matrix A from matrix C.

However, Loureiro et al teaches such an extension and derivation (see section 4.1).

At the time of the invention it would have been obvious to a person of ordinary skill in the art to use Loureiro et al's extending and deriving in Rijmen et al's ciphering method.

Motivation to do so would have been to hide a function represented on a matrix format.

As per claim 3, the modified Rijmen et al and Loureiro et al method discloses the step of deriving matrix A from matrix C includes: determining two permutation matrices P_1 , $P_2 \in Z_2^{kxk}$ such that all codewords in an [2k,k,d] error-correcting code, represented by the generator matrix $(I \parallel P_1CP_2)$, have a predetermined multi-bit weight; and using P_1CP_2 as matrix A (see Rijmen et al page 5 and Loureiro et al section 4.1).

Art Unit: 2137

As per claim 4, the modified Rijmen et al and Loureiro et al method discloses the cipher includes a round function with an S-box layer with S-boxes operating on m-bit sub-blocks, and the minimum predetermined multi-bit weight over all non-zero code words equals a predetermined m-bit weight (see Rijmen et al pages 5-6).

As per claim 8, the modified Rijmen et al and Loureiro et al method discloses a system for cryptographically converting an input data block into an output data block; the data blocks comprising n data bits; the system including: an input for receiving the input data block; a storage for storing a linear transformation matrix A, generated according to the method of claim 1, a cryptographic processor performing a linear transformation on the input data block or a derivative of the input data block using the linear transformation matrix A; and an output for outputting the processed input data block (see Rijmen et al as applied to claim 1 and Loureiro et al section 4.1).

5. Claims 2 and 5 are rejected under 35 U.S.C. 103(a) as being unpatentable over the modified Rijmen et al and Loureiro et al method as applied to claim 1 above, and further in view of FOLDOC.

Art Unit: 2137

As per claim 2, the modified Rijmen et al and Loureiro et al method discloses the step of extending matrix B with 2k-n columns includes (pseudo-) randomly generating 2k-n columns, each with k binary elements, and forming a test matrix consisting of the n-k columns of B and the 2k-n generating columns (see Loureiro et al section 4.1) and using the nonsingular matrix as matrix C (see Rijmen et al page 5).

The modified Rijmen et al and Loureiro et al method fails to disclose this process being done iteratively and checking whether the test matrix is nonsingular, and repeating until a nonsingular test matrix has been found.

However, FOLDOC discloses a method of brute force to find something (see page 1).

At the time of the invention it would have been obvious to a person of ordinary skill in the art to use FOLDOC's method of brute force to find the nonsingular matrix of the modified Rijmen et al and Loureiro et al method.

Motivation to do so would have been to be able to find every solution (see FOLDOC page 1).

As per claim 5, the modified Rijmen et al, Loureiro et al and FOLDOC method discloses the step of determining the two permutation matrices P_1 and P_2 includes iteratively generating

Art Unit: 2137

the matrices in a (pseudo-) random manner (see Loureiro et al section 4.1).

6. Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over the modified Rijmen et al and Loureiro et al method as applied to claim 1 above, and further in view of Isaka et al and Williams.

As per claim 6, the modified Rijmen et al and Loureiro et al method fails to disclose the cipher includes a round function operating on 32-bit blocks and wherein the step of generating a [n,k,d] error-correcting code includes: generating a binary extended Bose-Chaudhuri-Hocquenghem (XRCH) [64,36,12] code;

However, Isaka et al teaches such an XRCH code (see page 3).

At the time of the invention it would have been obvious to a person of ordinary skill in the art to use Isaka et al's XRCH code as the error-correcting code of the modified Rijmen et al and Loureiro et al method.

Motivation to do so would have been that these codes achieve unequal error protection (see Isaka et al abstract page 1).

The modified Rijmen et al, Loureiro et al, and İsaka et al method fails to disclose shortening this code to a [60,32,12] shortened XRCH code by deleting four rows.

Art Unit: 2137

However, Williams discloses shortening error-correcting codes (see page 38).

At the time of the invention it would have been obvious to a person of ordinary skill in the art to use Williams' method of shortening error-correcting codes to shorten the codes of the modified Rijmen et al, Loureiro et al, and Isaka et al method.

Motivation to do so would have been that shortening codes enhances flexibility (see Williams page 38).

Conclusion

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Garay et al (US 5991414 A) discloses a method of symmetric ciphering with a linear transformation, and Shimada (US 5640455 A) discloses a method of symmetric key encryption using linear transformations.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael Pyzocha whose telephone number is (571) 272-3875. The examiner can normally be reached on 7:00am - 4:30pm first Fridays of the bi-week off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Andrew Caldwell can be reached on (571) 272-3868. The fax phone number for the

Art Unit: 2137

organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

MJP

ANDREW CALDWELL
SUPERVISORY PATENT EXAMINER

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Page 8